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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/671,261	09/28/2000	YUTAKA TAKEUCHI	106375	8216
25944	7590	08/28/2002	EXAMINER	
OLIFF & BERRIDGE, PLC P.O. BOX 19928 ALEXANDRIA, VA 22320			DI GRAZIO, JEANNE A	
ART UNIT		PAPER NUMBER		
2871		DATE MAILED: 08/28/2002		

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary	Application No.	Applicant(s)
	09/671,261	TAKEUCHI, YUTAKA
	Examiner	Art Unit
	Jeanne A. Di Grazio	2871

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
- Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

1) Responsive to communication(s) filed on _____.

2a) This action is **FINAL**. 2b) This action is non-final.

3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

4) Claim(s) 1-4 is/are pending in the application.

4a) Of the above claim(s) _____ is/are withdrawn from consideration.

5) Claim(s) _____ is/are allowed.

6) Claim(s) 1-4 is/are rejected.

7) Claim(s) _____ is/are objected to.

8) Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

9) The specification is objected to by the Examiner.

10) The drawing(s) filed on 28 September 2000 is/are: a) accepted or b) objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).

11) The proposed drawing correction filed on _____ is: a) approved b) disapproved by the Examiner.
If approved, corrected drawings are required in reply to this Office action.

12) The oath or declaration is objected to by the Examiner.

Priority under 35 U.S.C. §§ 119 and 120

13) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).

a) All b) Some * c) None of:

1. Certified copies of the priority documents have been received.
2. Certified copies of the priority documents have been received in Application No. _____.
3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

14) Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application).
a) The translation of the foreign language provisional application has been received.

15) Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121.

Attachment(s)

1) Notice of References Cited (PTO-892) 4) Interview Summary (PTO-413) Paper No(s). _____.

2) Notice of Draftsperson's Patent Drawing Review (PTO-948) 5) Notice of Informal Patent Application (PTO-152)

3) Information Disclosure Statement(s) (PTO-1449) Paper No(s) _____.

6) Other: _____

DETAILED ACTION

Priority

Applicant claims priority to Japanese Patent Application No. 11-275250 (filed September 28, 1999) pursuant to 35 U.S.C. Sec. 119.

Double Patenting

The nonstatutory double patenting rejection is based on a judicially created doctrine grounded in public policy (a policy reflected in the statute) so as to prevent the unjustified or improper timewise extension of the "right to exclude" granted by a patent and to prevent possible harassment by multiple assignees. See *In re Goodman*, 11 F.3d 1046, 29 USPQ2d 2010 (Fed. Cir. 1993); *In re Longi*, 759 F.2d 887, 225 USPQ 645 (Fed. Cir. 1985); *In re Van Ornum*, 686 F.2d 937, 214 USPQ 761 (CCPA 1982); *In re Vogel*, 422 F.2d 438, 164 USPQ 619 (CCPA 1970); and, *In re Thorington*, 418 F.2d 528, 163 USPQ 644 (CCPA 1969).

A timely filed terminal disclaimer in compliance with 37 CFR 1.321(c) may be used to overcome an actual or provisional rejection based on a nonstatutory double patenting ground provided the conflicting application or patent is shown to be commonly owned with this application. See 37 CFR 1.130(b).

Effective January 1, 1994, a registered attorney or agent of record may sign a terminal disclaimer. A terminal disclaimer signed by the assignee must fully comply with 37 CFR 3.73(b).

Claims 1-4 are rejected under the judicially created doctrine of obviousness-type double patenting as being unpatentable over claims 1-3, 5-9, and 11 of U.S. Patent No. 5,986,739.

Per claim 1: a substrate, a plurality of color material layers arranged on the substrate, a light-shielding layer surrounding each color material layer, a protective layer covering the color material layers and the light-shielding layer, and a plurality of electrode strips [1] arranged on the protective layer and extending from a formation region [V] to an unformed region [H], an electrode width of an electrode strip in a step portion of the protective layer narrower than an electrode width of an electrode strip in the protective layer in an effective region of the LCD [FIG. 1, and Col. 3, Lines 22-38 and Col. 3, Lines 48-50]. A substrate is common in the art of liquid crystal technology. Color material layers are common in the color liquid crystal art. A

light-shielding layer is commonly used in the art to improve resolution. A protective layer is common in the art to protect various layers and or films used along with the liquid crystal.

It would have been obvious to one of ordinary skill in the art to add these common elements along with the other elements of the claimed invention when manufacturing a color LCD.

Per Claim 2: a part of an electrode width of an electrode strip within the unformed region of the protective layer being equal to an electrode width of an electrode strip on the protective layer within the effective region.

Kobayashi does not have equal electrode widths within the its regions; however, Kobayashi does teach that widths can be modified for the purpose of being able to test at appropriate times for shorts or breaks among the various display electrodes. It would have been obvious, at the time the invention was made, to set the electrode widths equal to one another in the unformed and effective regions in order to better test for shorts and breaks among the various electrodes and to prevent inaccurate or bogus voltage readings that could indicate a short when no short actually exists. It would have been obvious to one of ordinary skill in the art to thus modify Kobayashi as stated and to arrive at applicant's invention.

Per claim 3: a pair of sides of an electrode strip that form an outline of an electrode strip in a longitudinal direction thereof in the unformed region of the protective layer lying in extensions of a pair of sides of an electrode strip that forms an outline of an electrode strip in the longitudinal direction thereof in the protective layer in the effective region.

Sides are an inherent characteristic of strips.

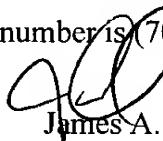
Per claim 4: electronic equipment comprising the LCD of claim 1 [Col. 5, Lines 2-5].

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Jeanne A. Di Grazio whose telephone number is (703)305-7009. The examiner can normally be reached on M-F.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, William Sikes can be reached on (703)308-4842. The fax phone numbers for the organization where this application or proceeding is assigned are (703)746-8741 for regular communications and (703)746-8741 for After Final communications.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is (703)308-0956.

Jeanne Andrea Di Grazio



James A. Dudek, Primary Examiner

JDG

August 21, 2002